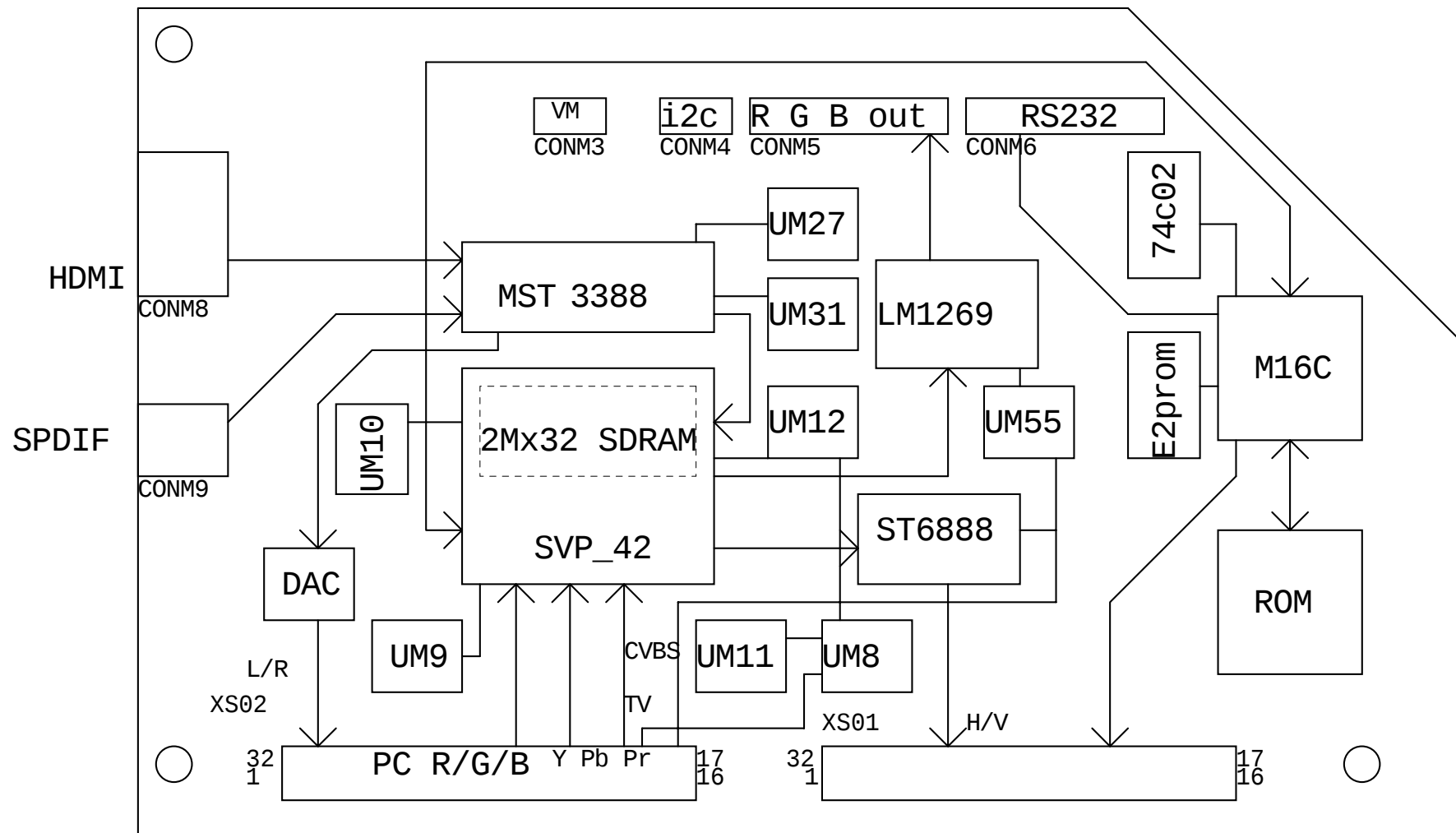
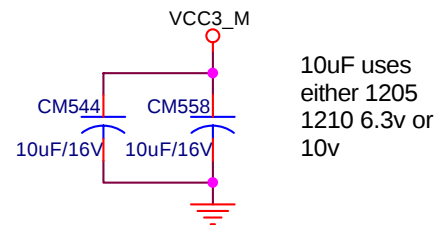
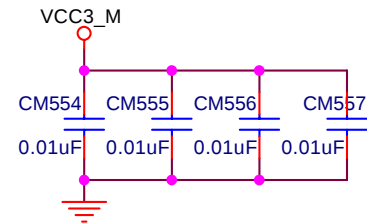
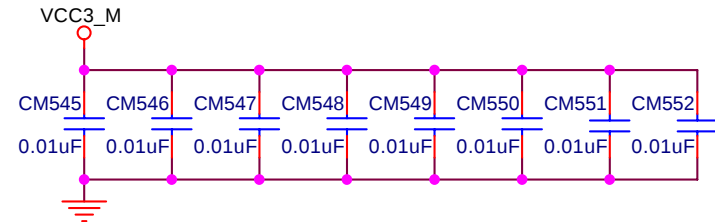
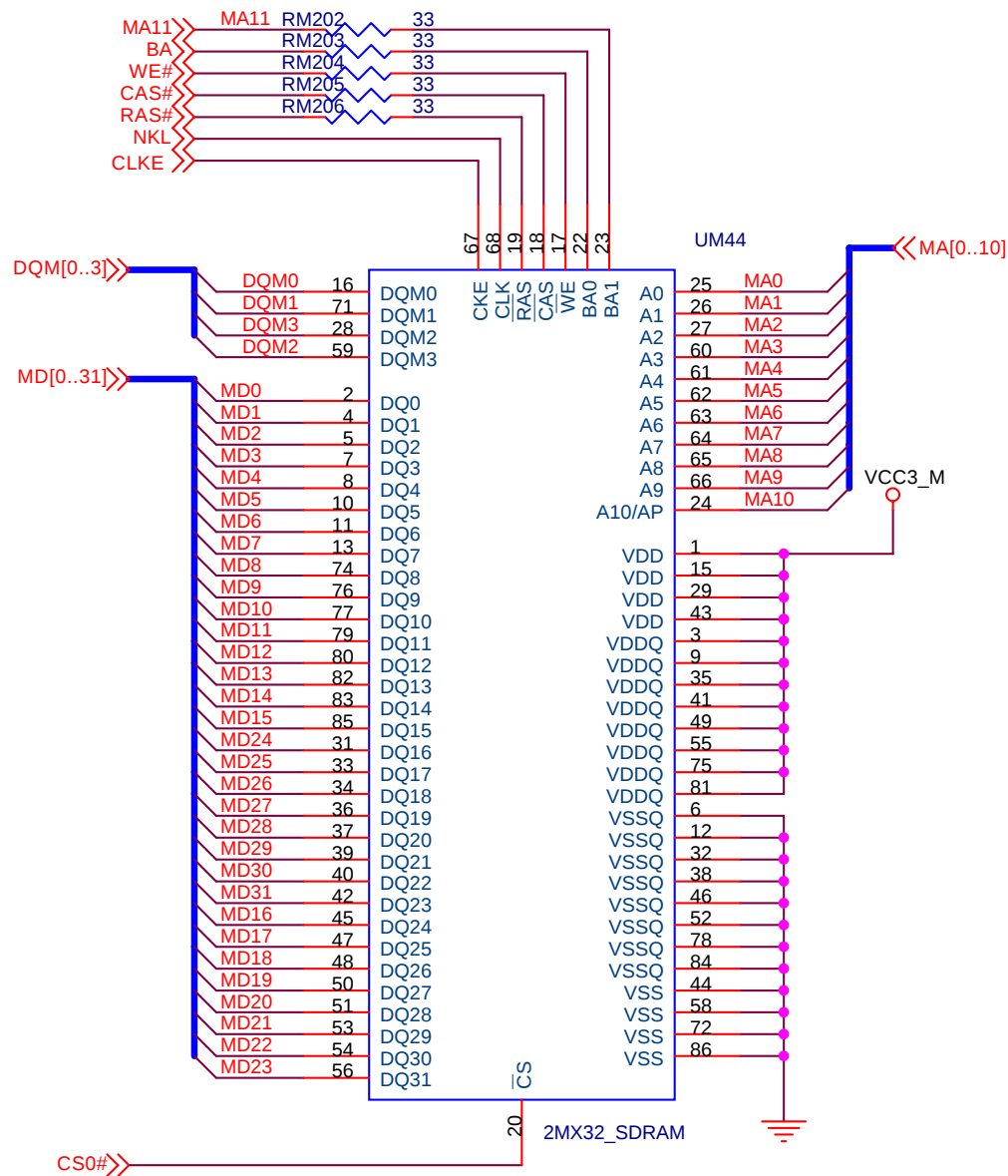


FB510E Block Diagram

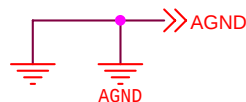


		Title	
Schematic Name		Rev	
FB510		E	
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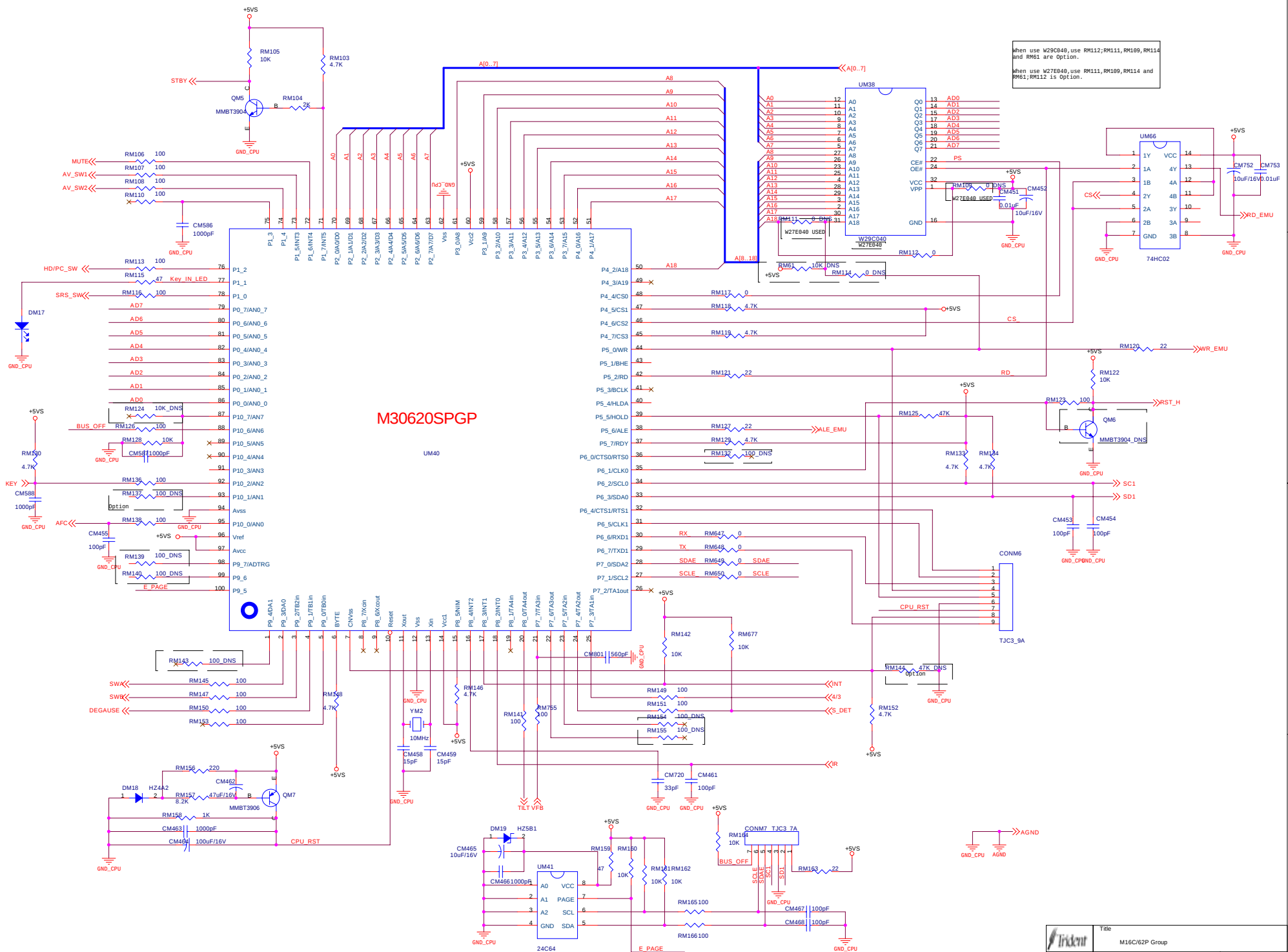


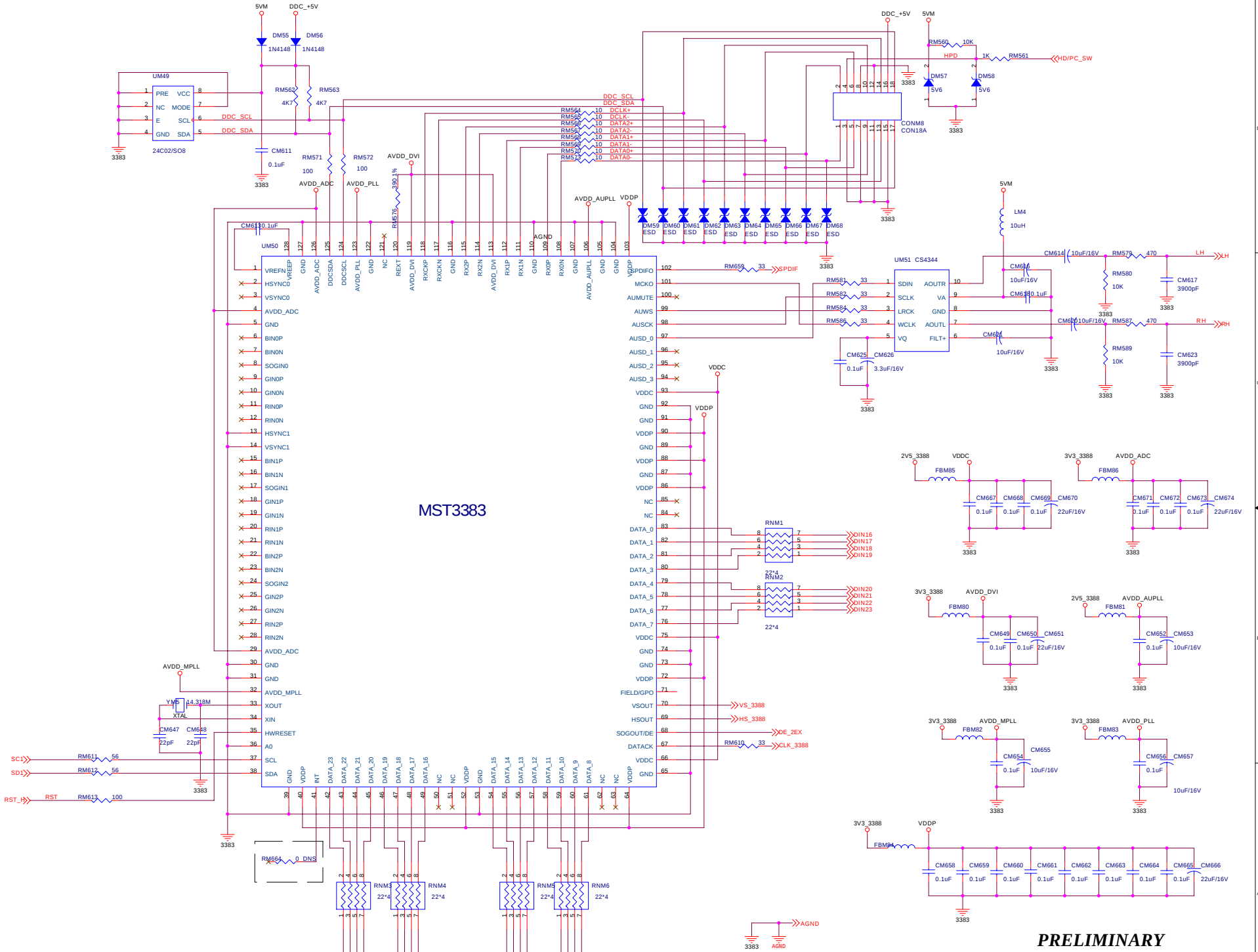
10uF uses
either 1205
1210 6.3v or
10v

SDRAM DECOUPLING CAPACITORS



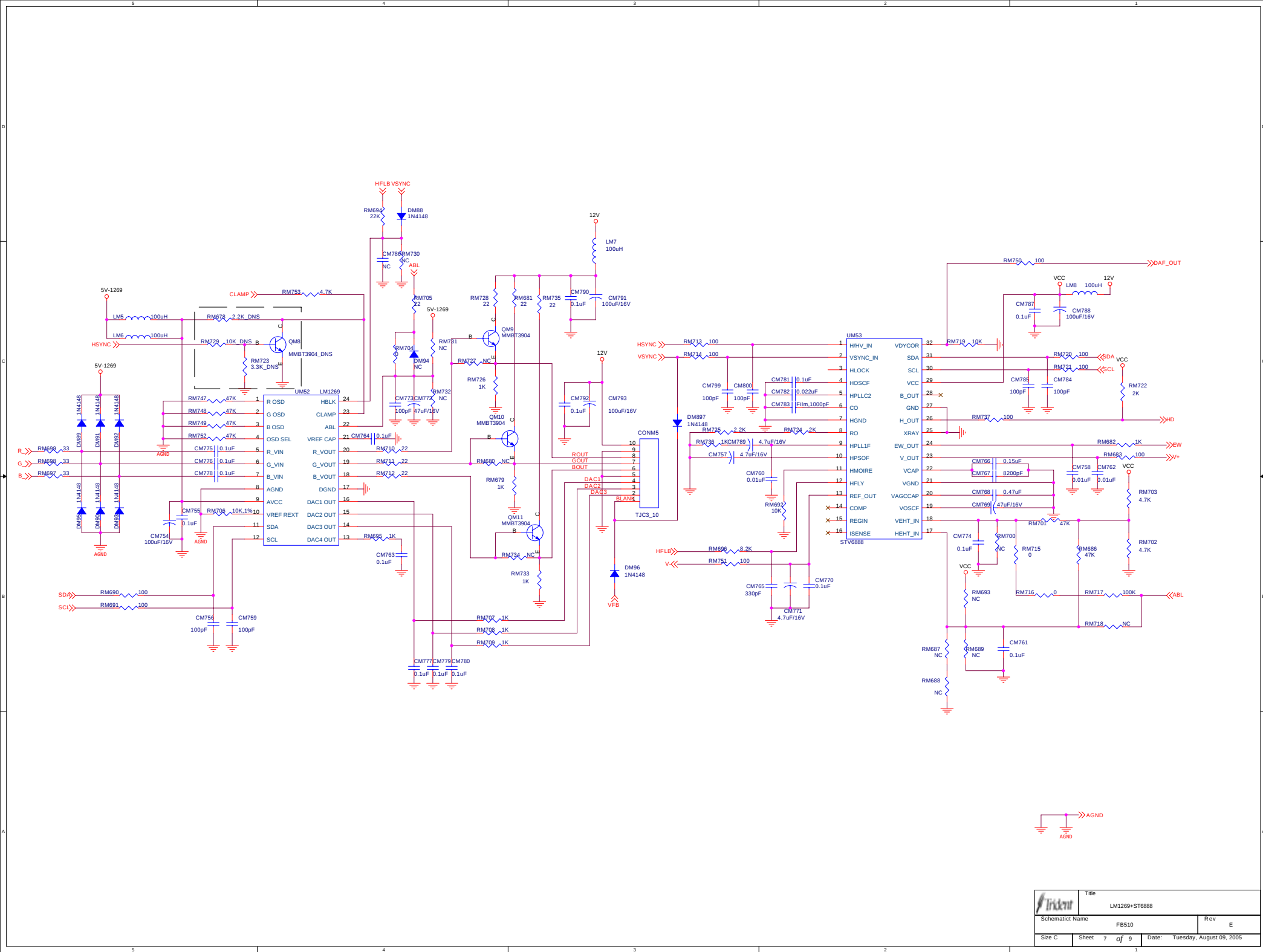
		Title	
		2M*32 SDRAM	
Schematic Name		FB510	Rev E
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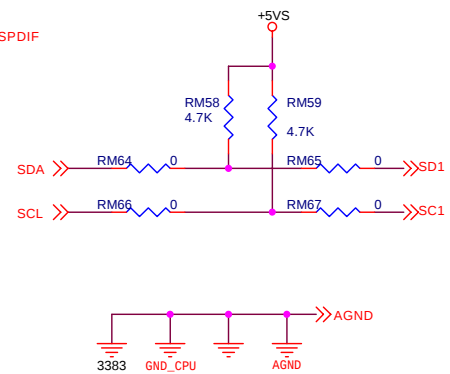
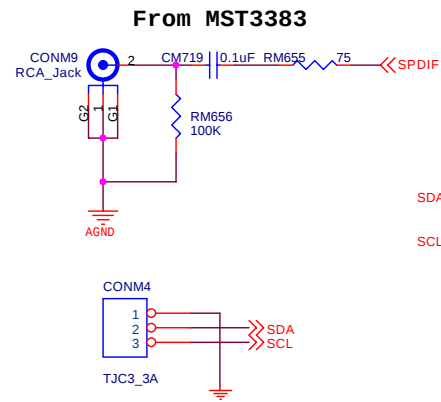
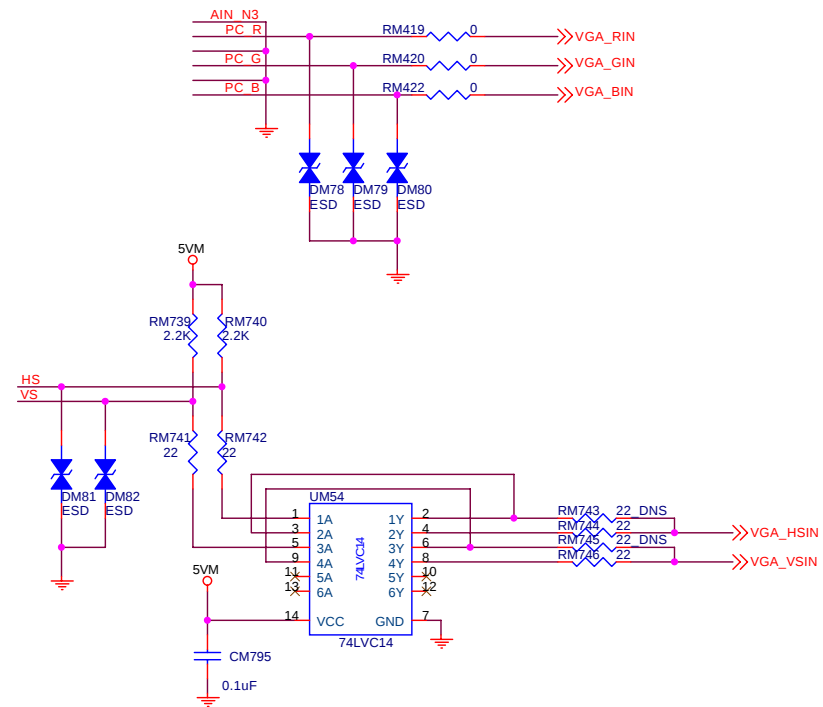
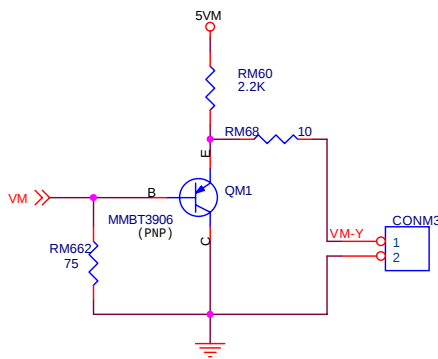
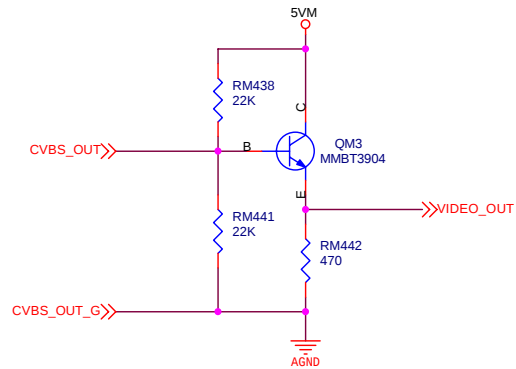
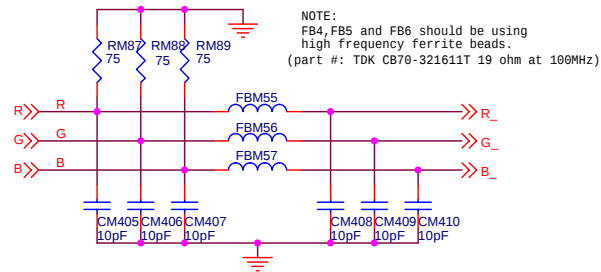
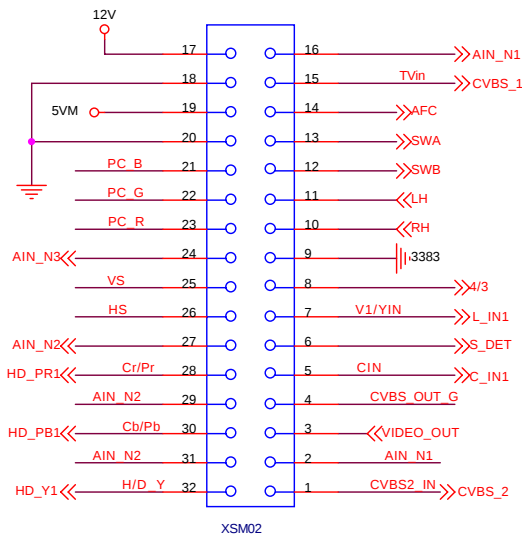
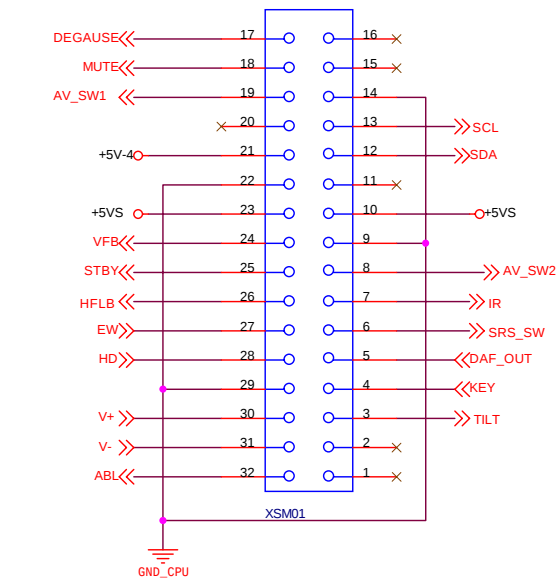




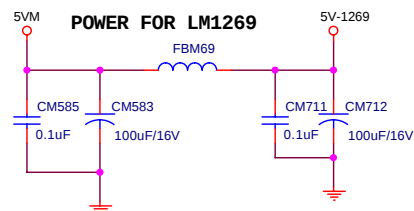
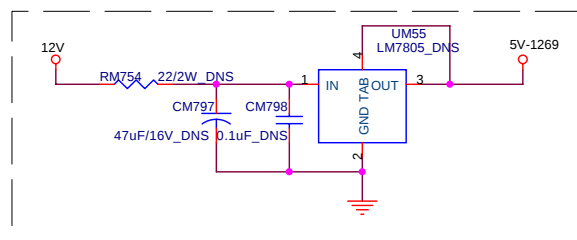
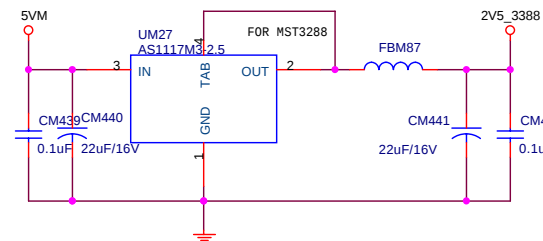
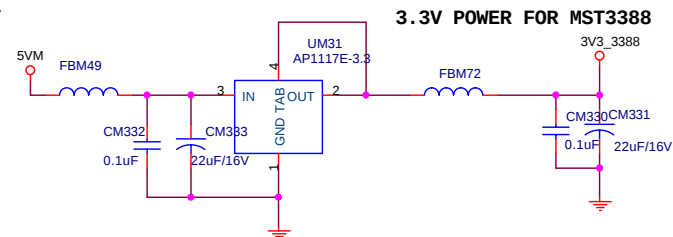
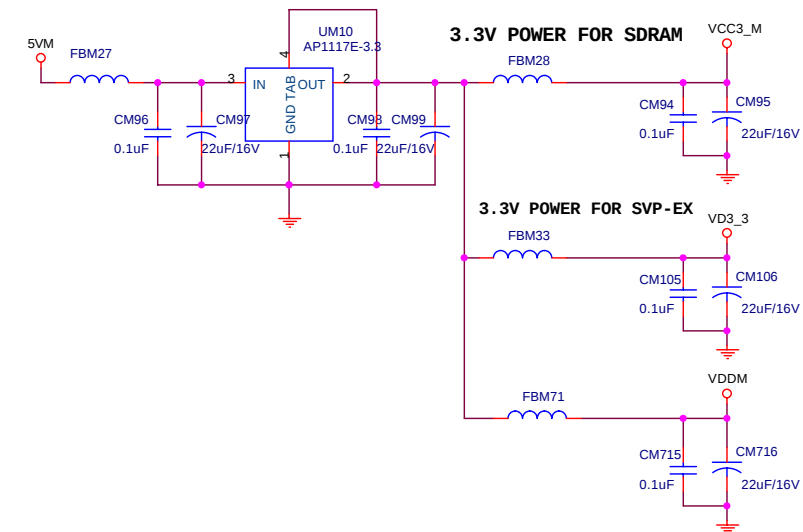
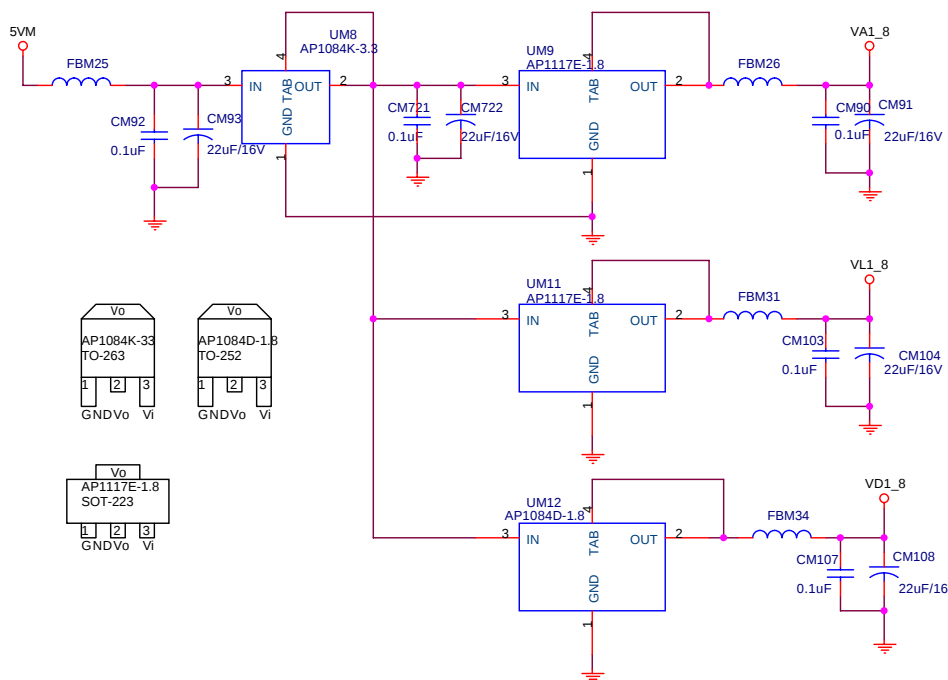
PRELIMINARY

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- TP1 TP_OBLONG
- TP2 TP_OBLONG
- TP3 TP_OBLONG
- TP4 TP_OBLONG

PRELIMINARY

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